

PATENT ABSTRACTS OF JAPAN

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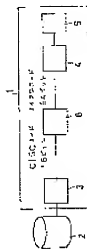
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(54) COMPUTER FOR STORING INSTRUCTION CODE CONVERSION HIERARCHY

(57) Abstract:

PURPOSE: To increase the processing speed by securing such a constitution where an instruction code converter inputs the instruction code of a machine word format stored in a 1st memory and then inputs the code to a 2nd memory after converting it into the corresponding microcode. CONSTITUTION: An instruction code converter 6 is provided between a 1st memory 3 and a 2nd memory 4. Then the converter 6 inputs the instruction code of a machine word format stored in the memory 3 and then outputs this code to the memory 4 after converting it into the corresponding microcode. The converting speed of the converter 6 is preferably set at the reading speed of the main memory 3. Thus no conversion is required at every execution of an instruction code. Consequently, each instruction code is carried out at high speed and a high speed computer is obtained.



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